



TITLE: METHOD OF FABRICATING A TRANSISTOR ON A SUBSTRATE TO OPERATE AS A FULLY DEPLETED STRUCTURE

INVENTOR'S NAME: Leonard Forbes, et al.

SERIAL NO.: 10/057,225 DOCKET NO.: 303.506US4

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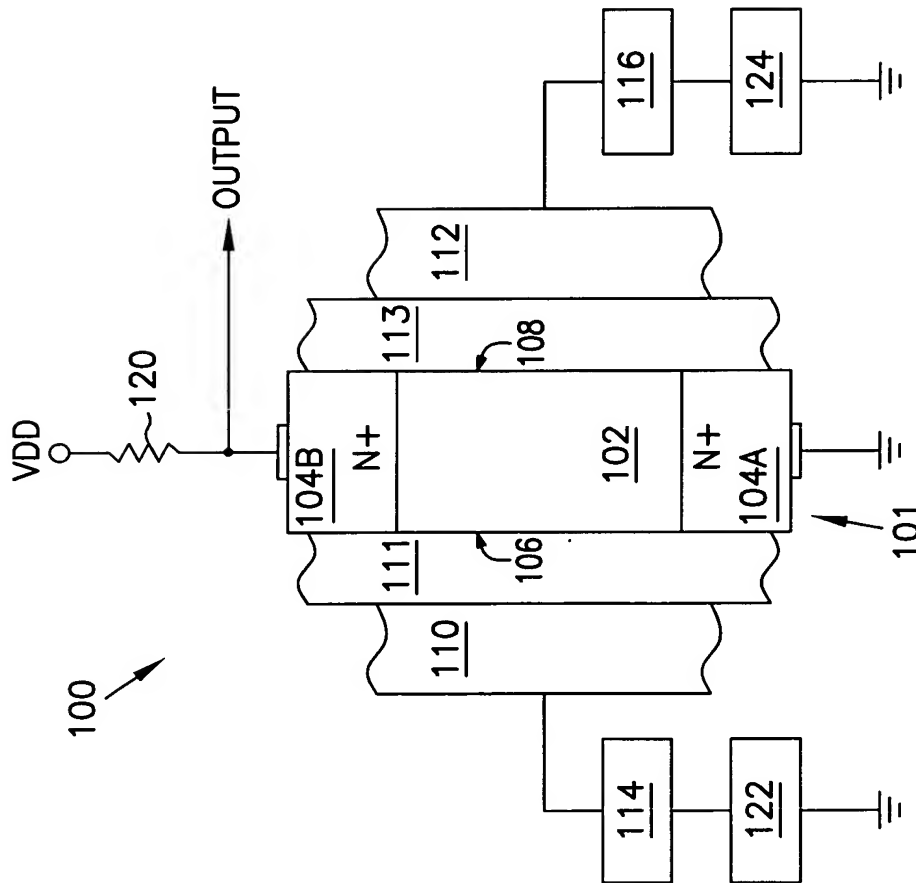


FIG. 1A

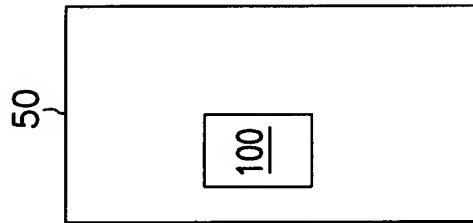


FIG. 1B

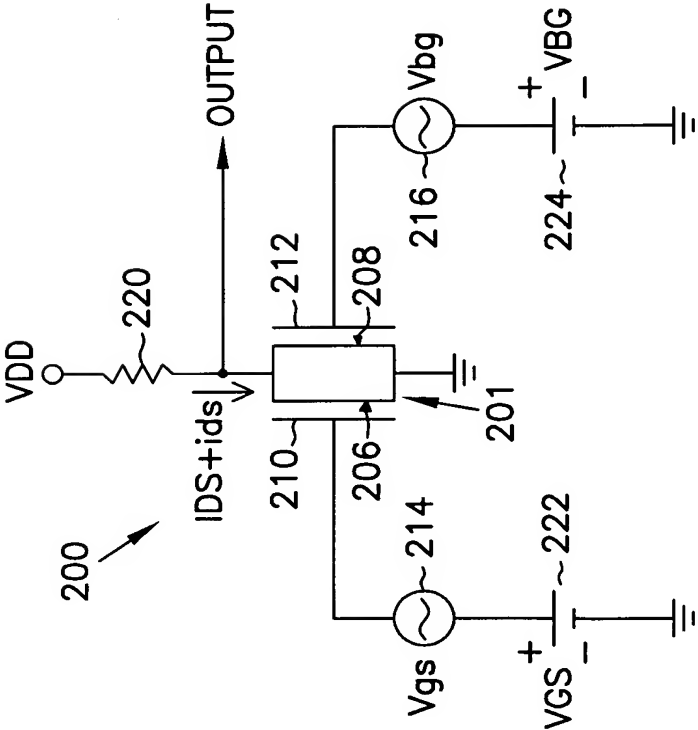


FIG. 2

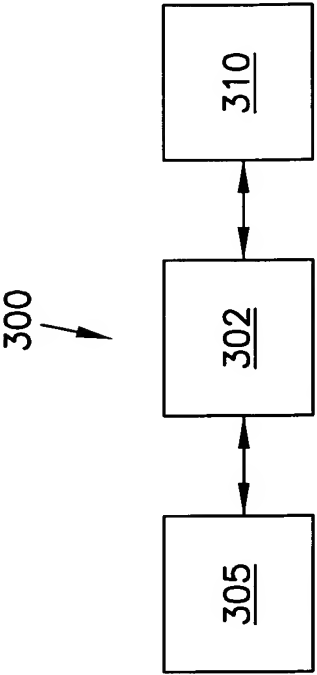


FIG. 3

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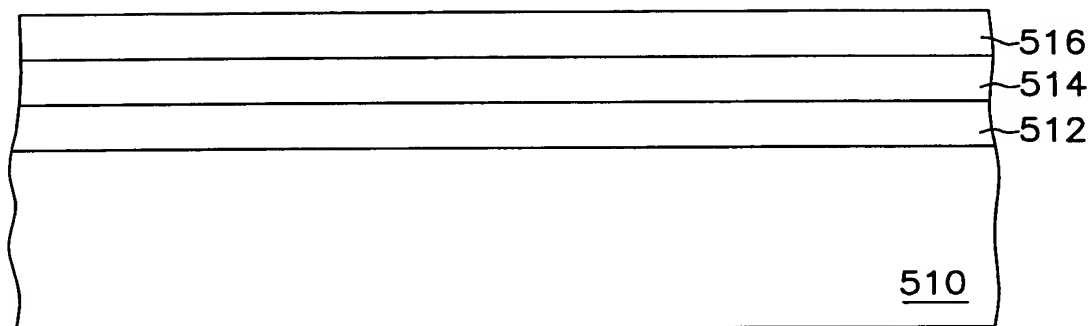


FIG. 4A

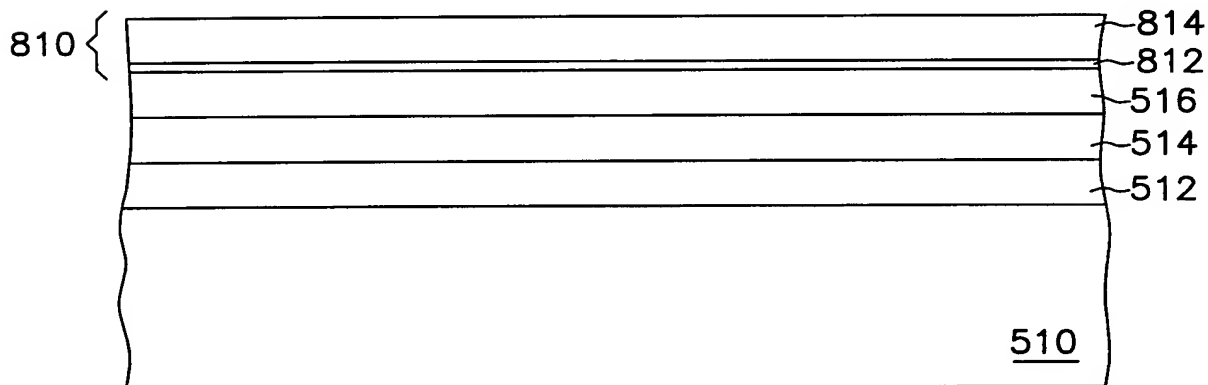


FIG. 4B

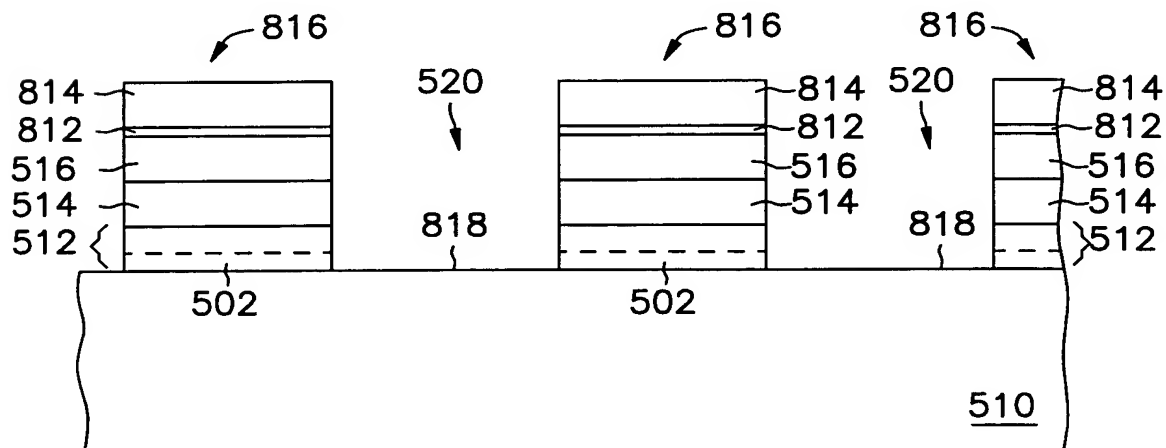
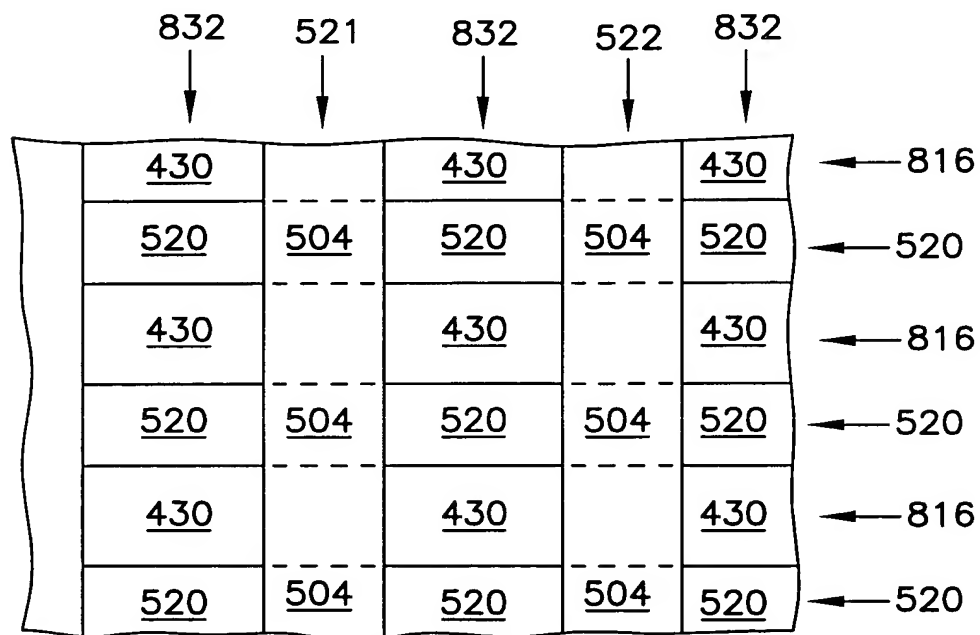
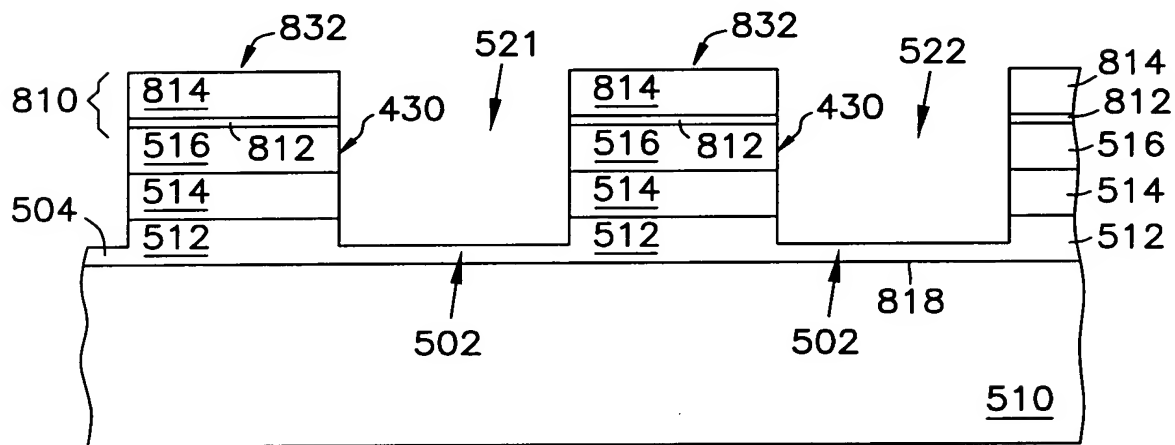
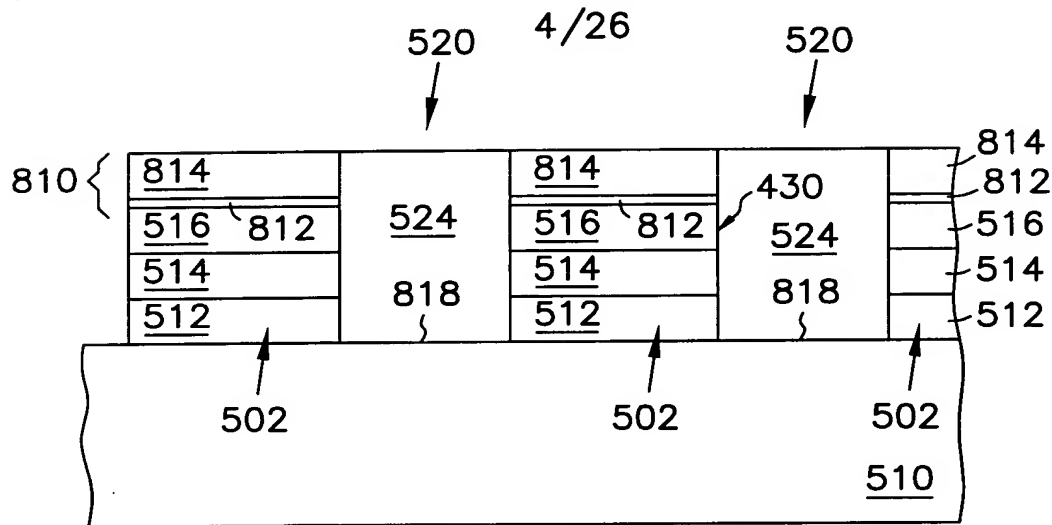


FIG. 4C



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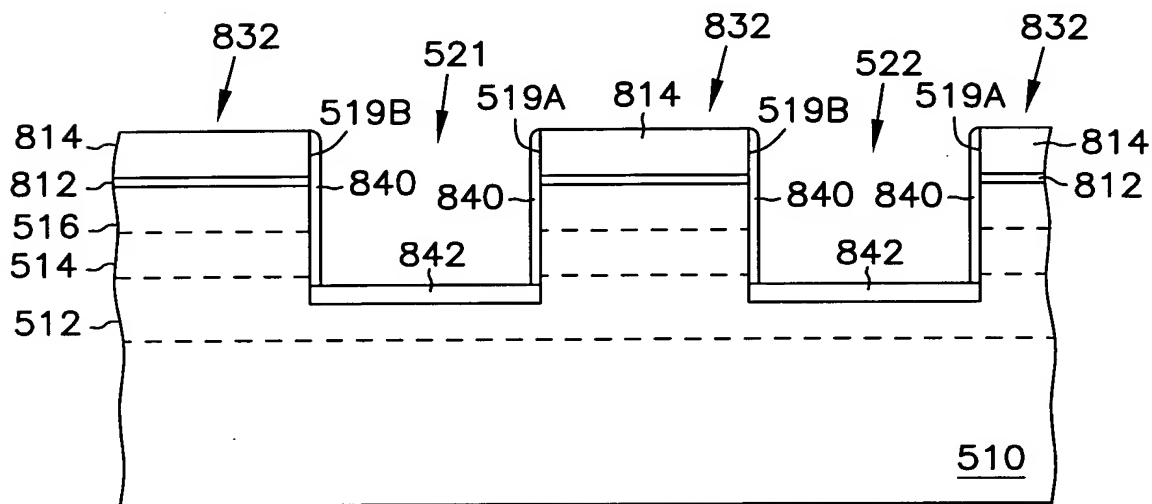


FIG. 4G

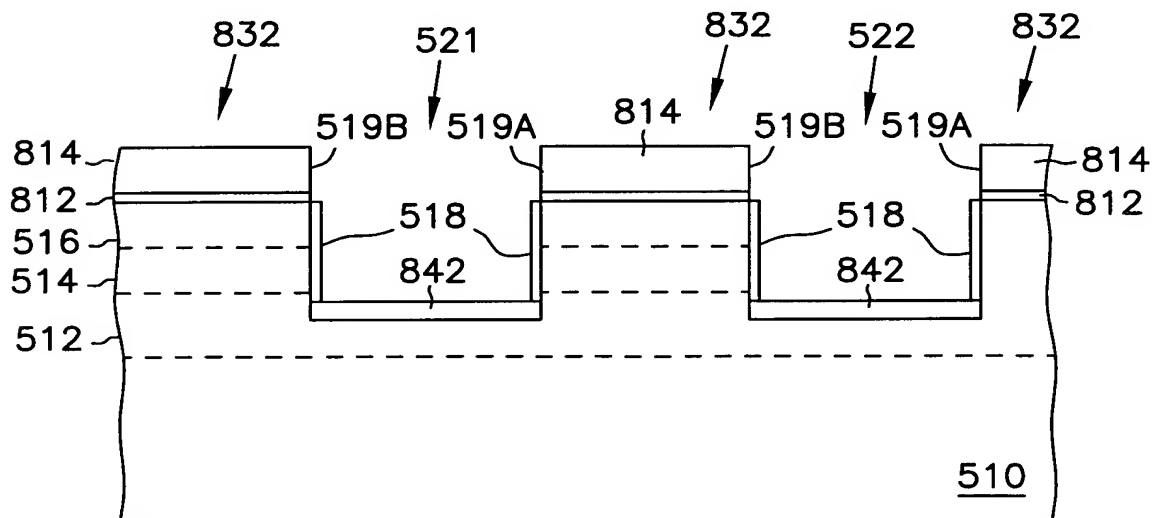


FIG. 4H

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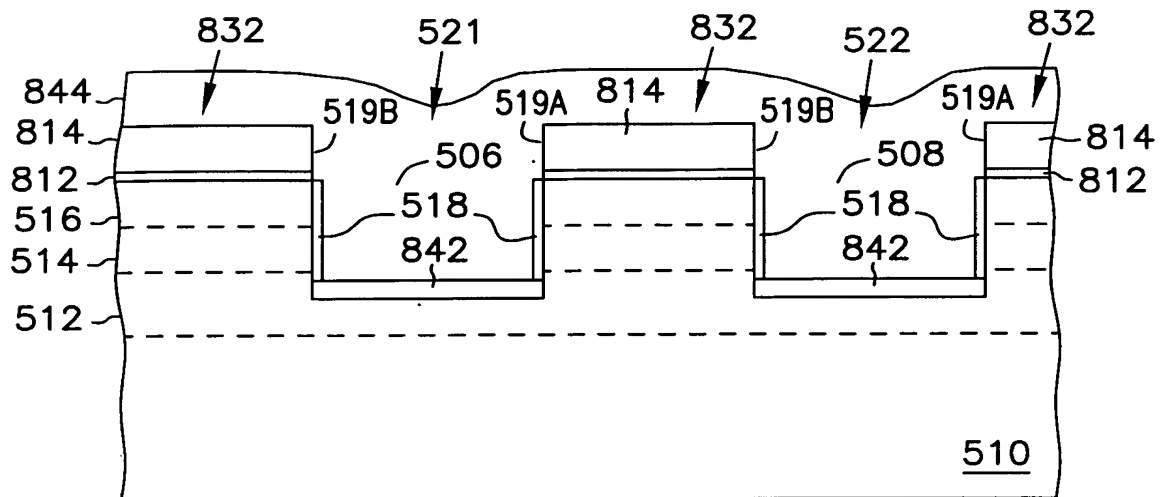


FIG. 4I

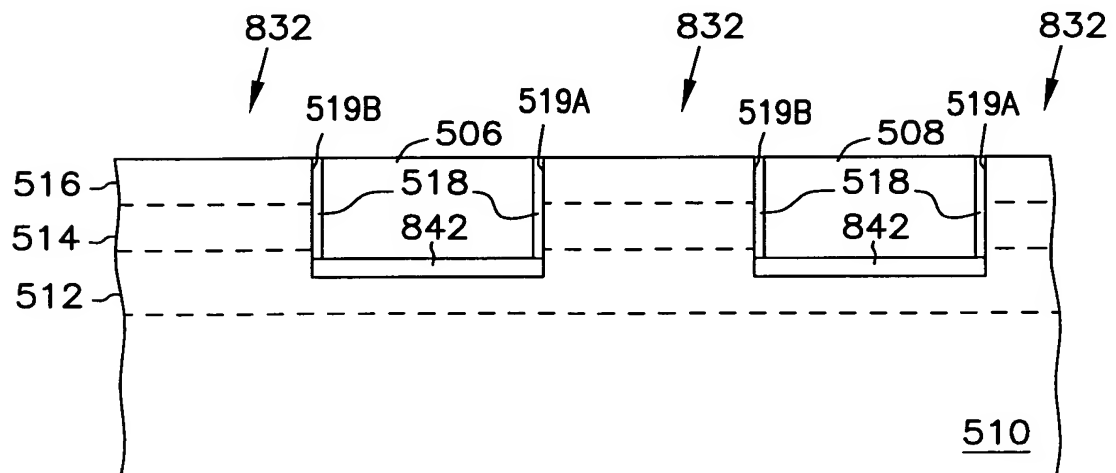


FIG. 4J

FIG. 4K

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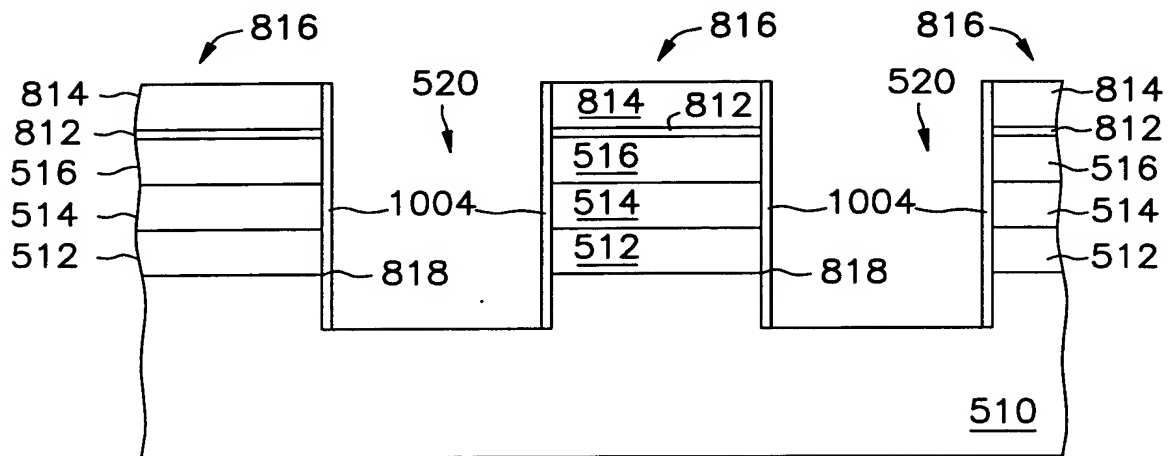


FIG. 5A

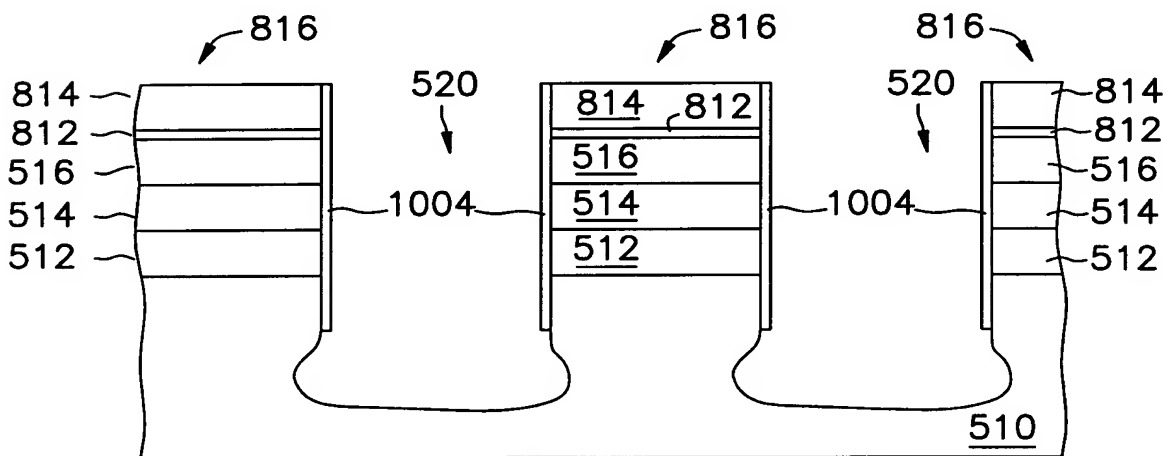


FIG. 5B

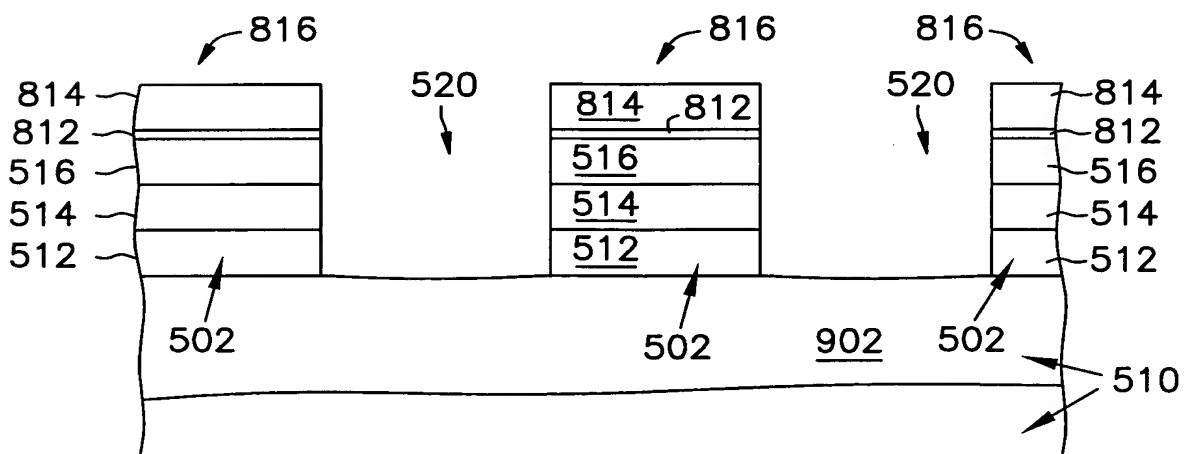


FIG. 5C



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FIG. 6A

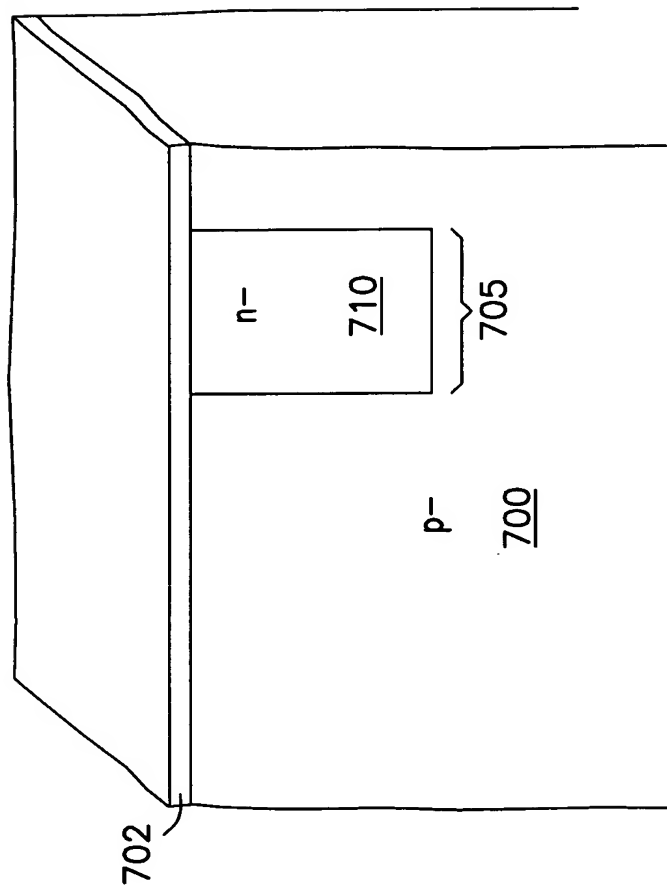
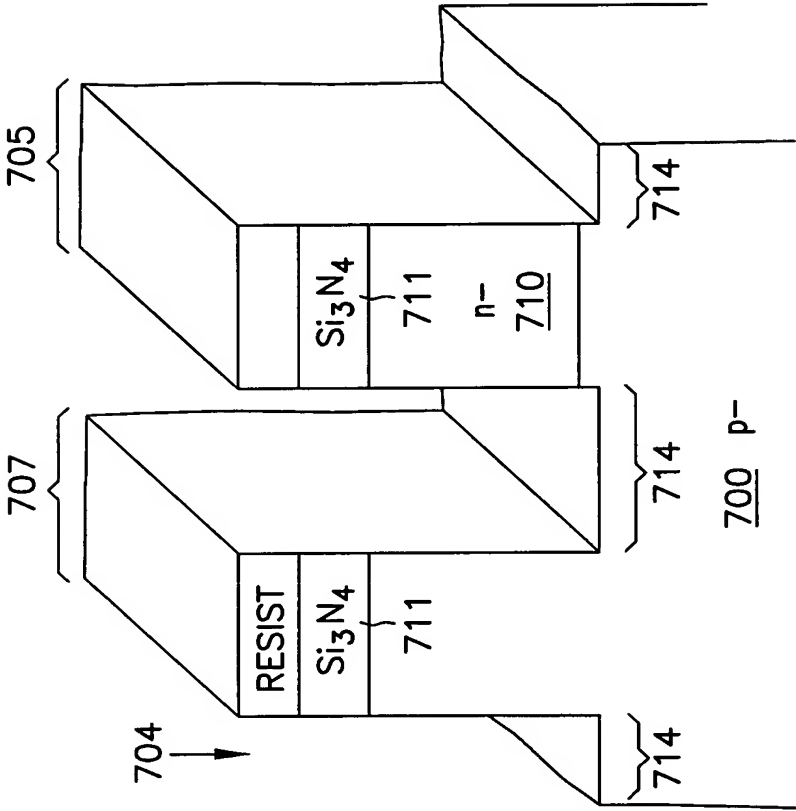


FIG. 6B



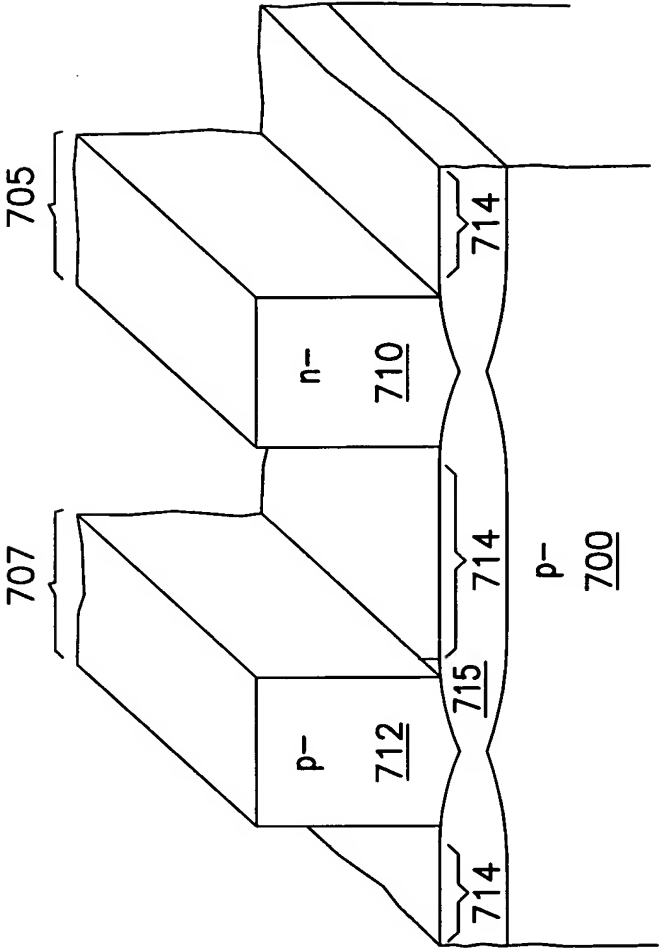


FIG. 6C



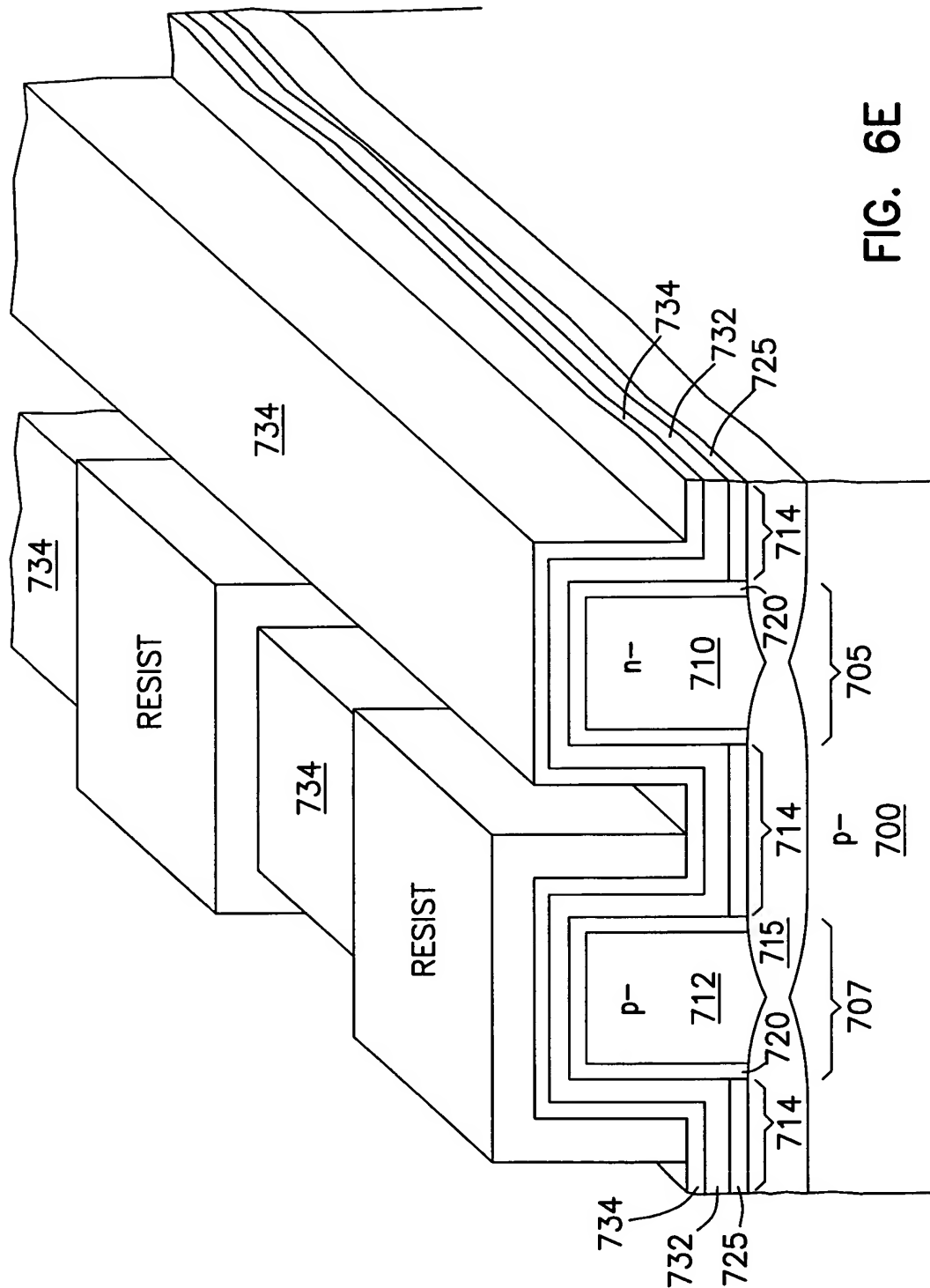
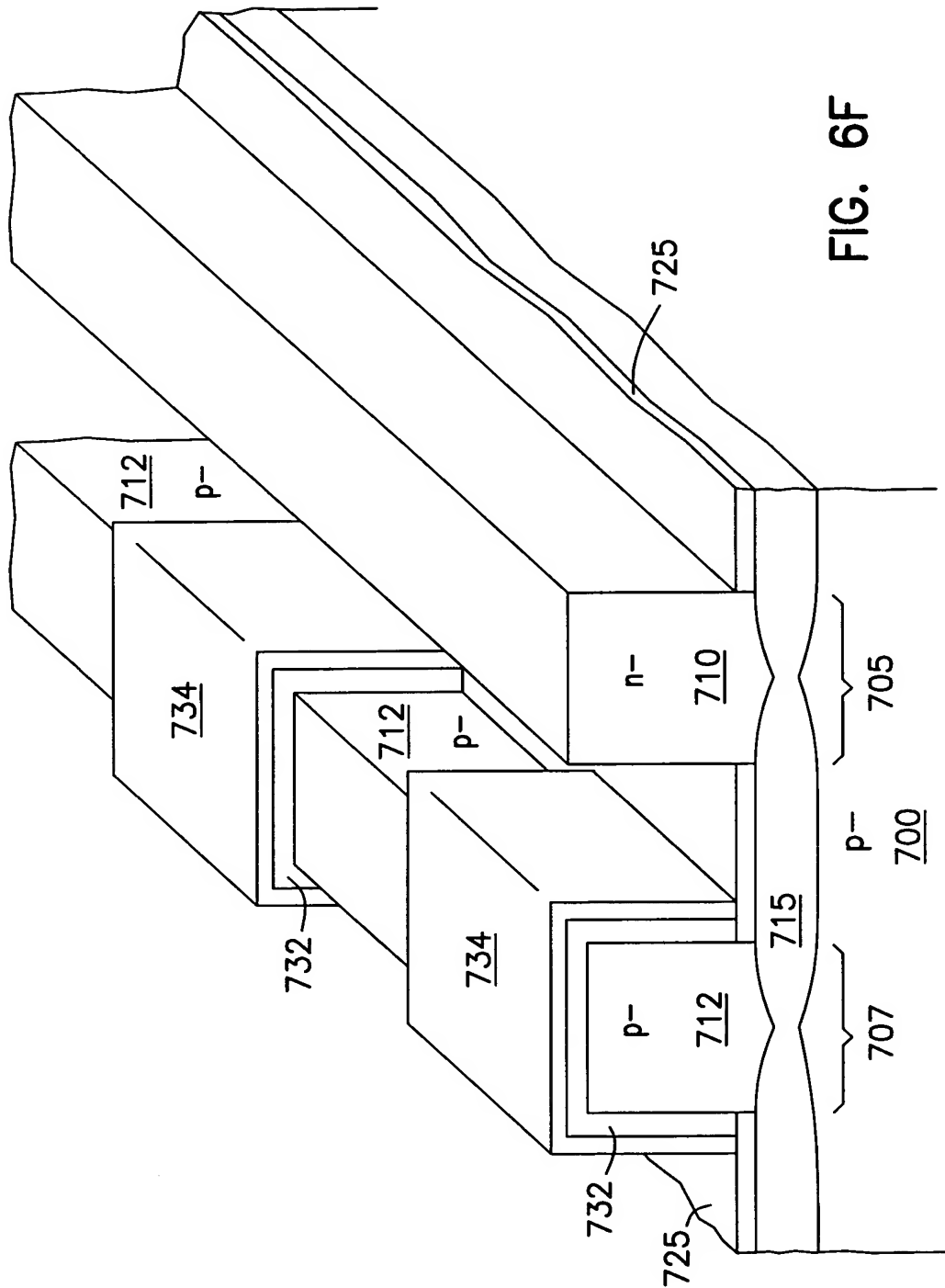
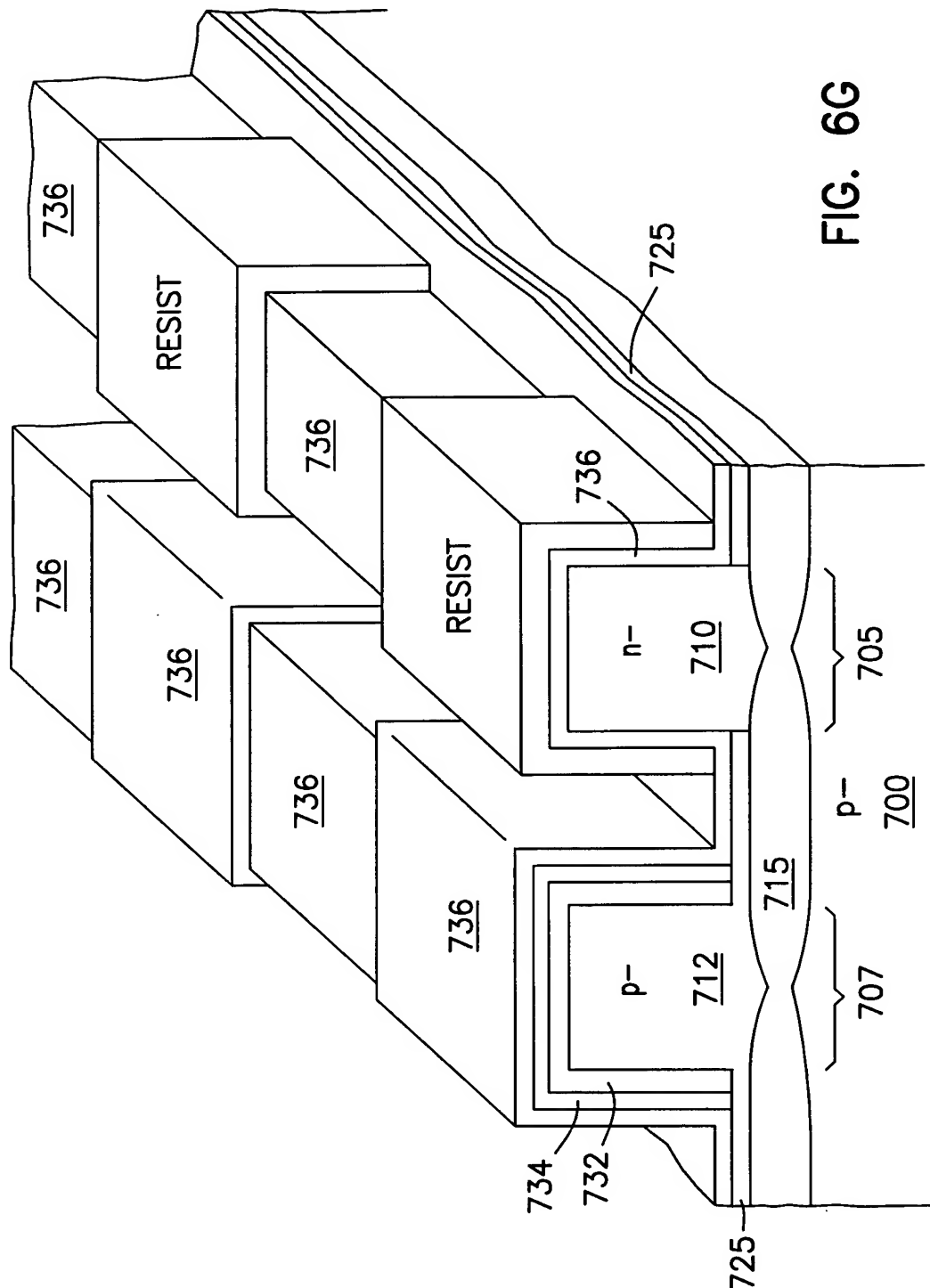
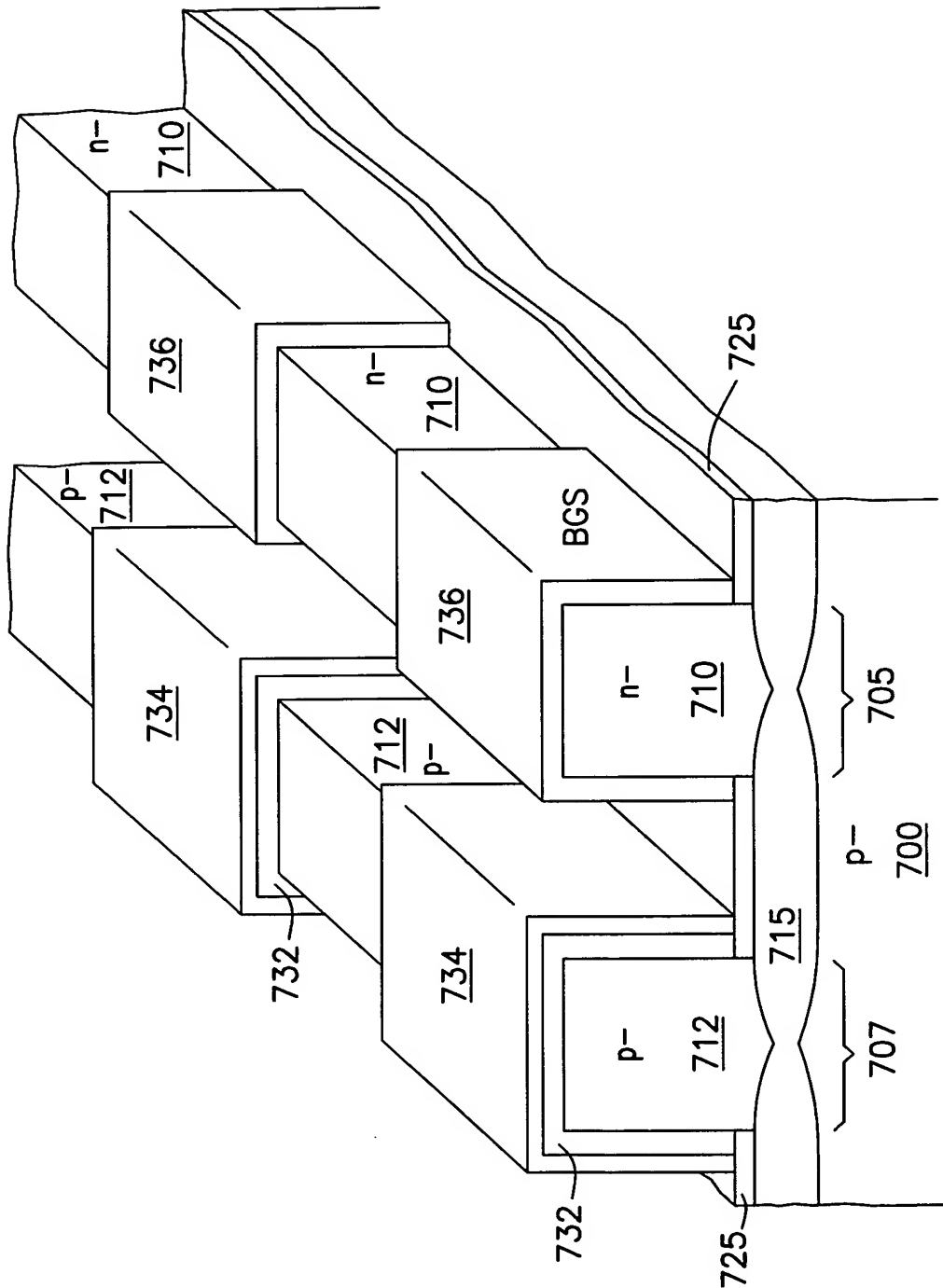


FIG. 6E





**FIG. 6G**





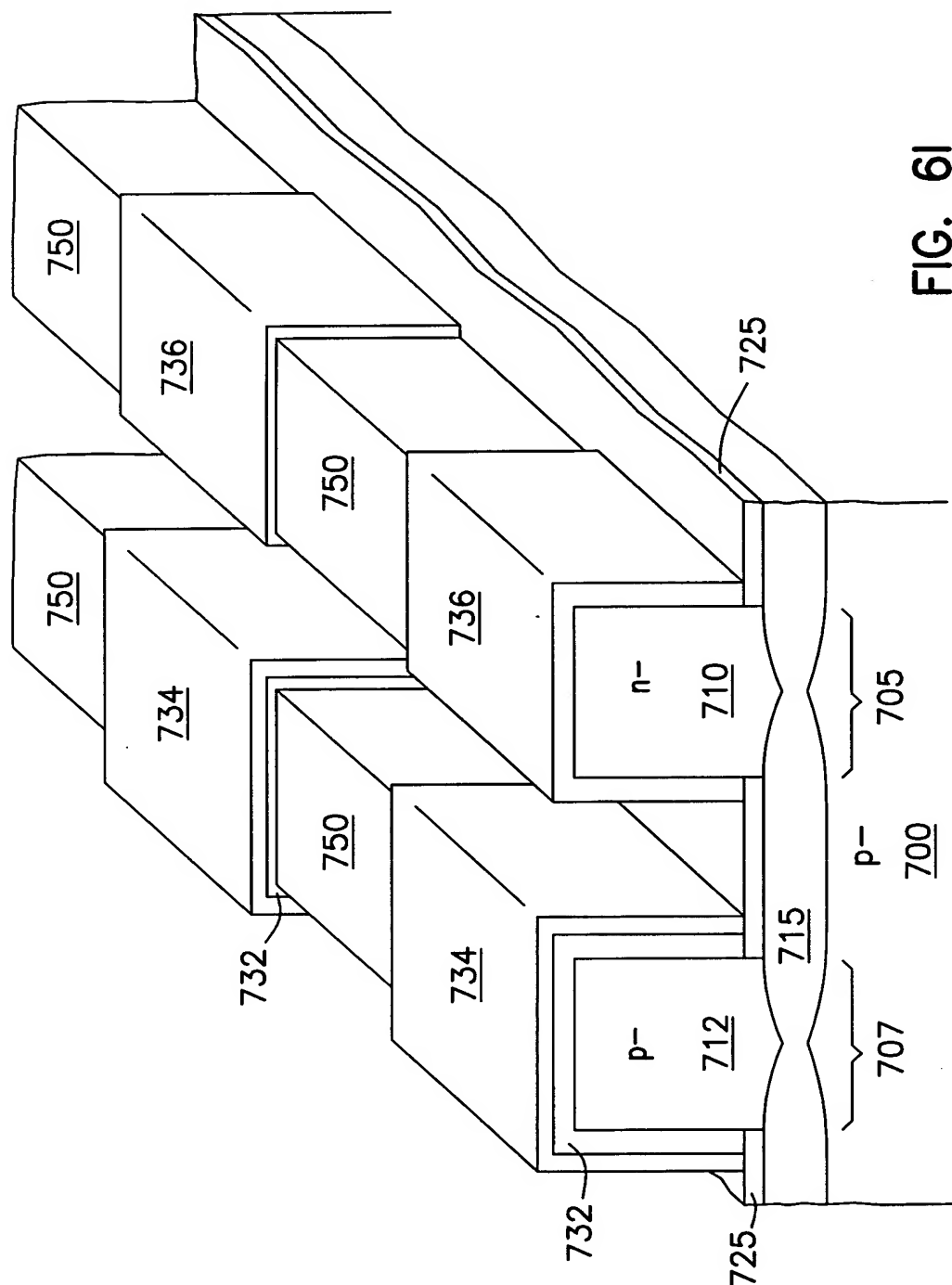


FIG. 61

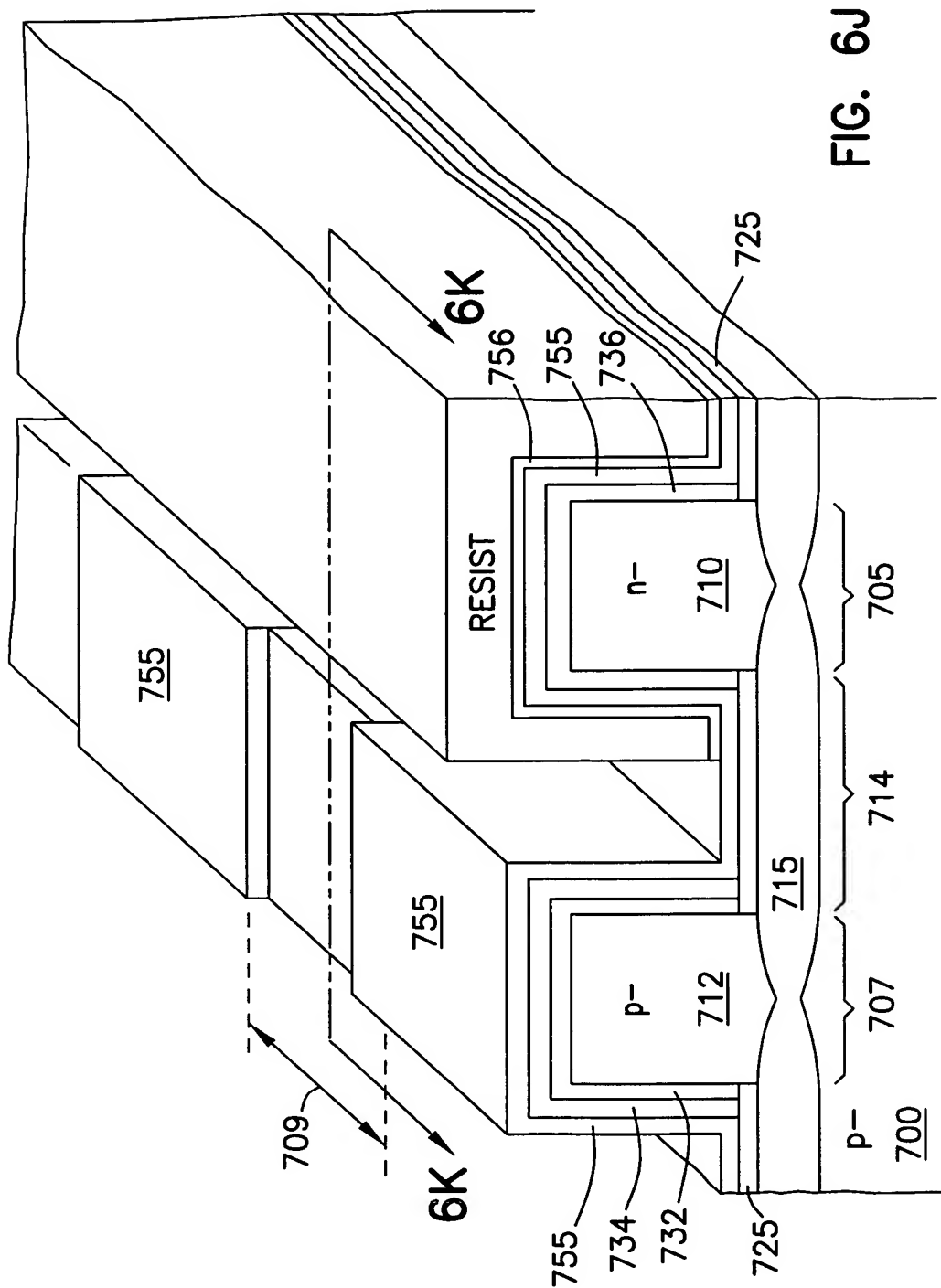
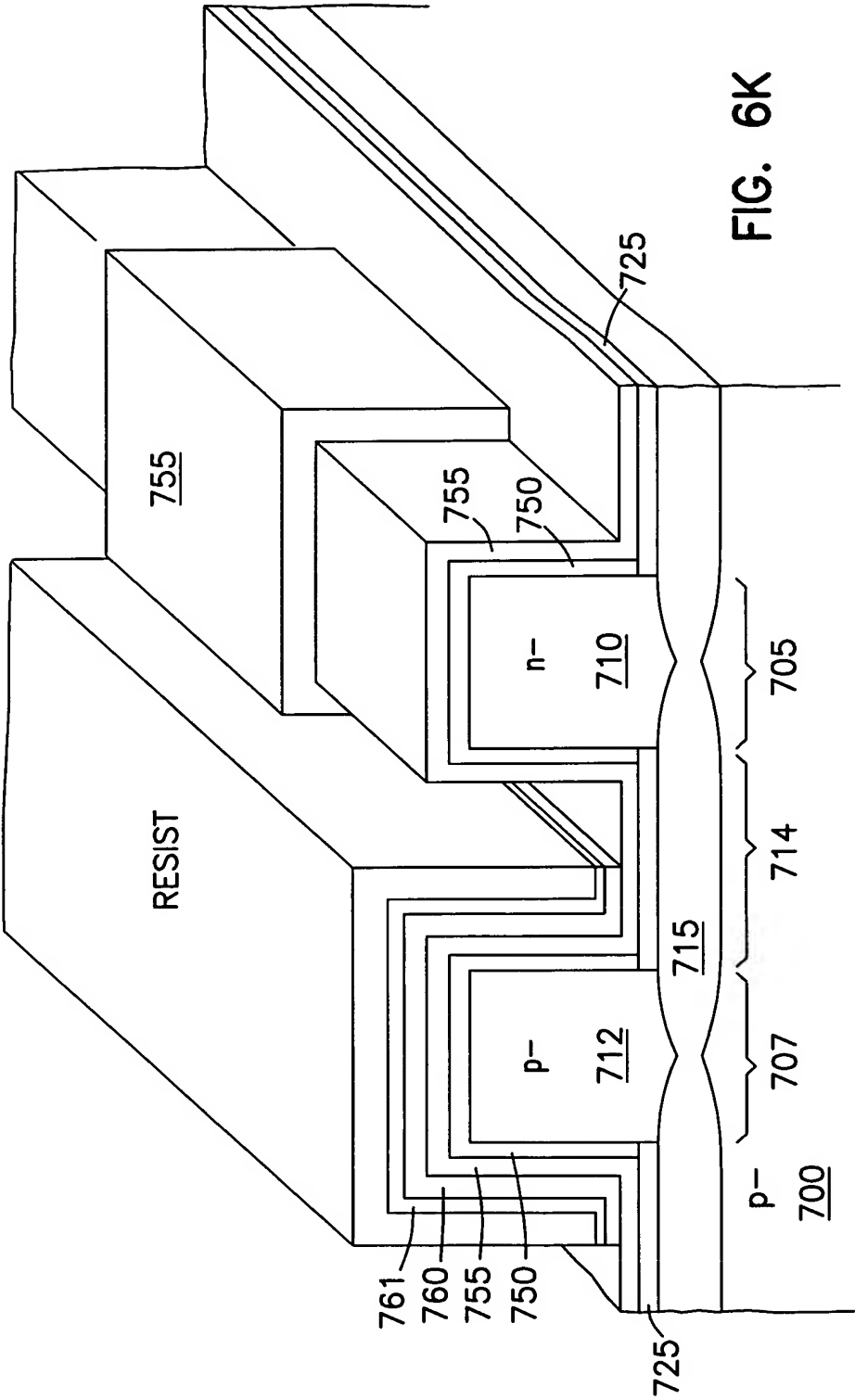
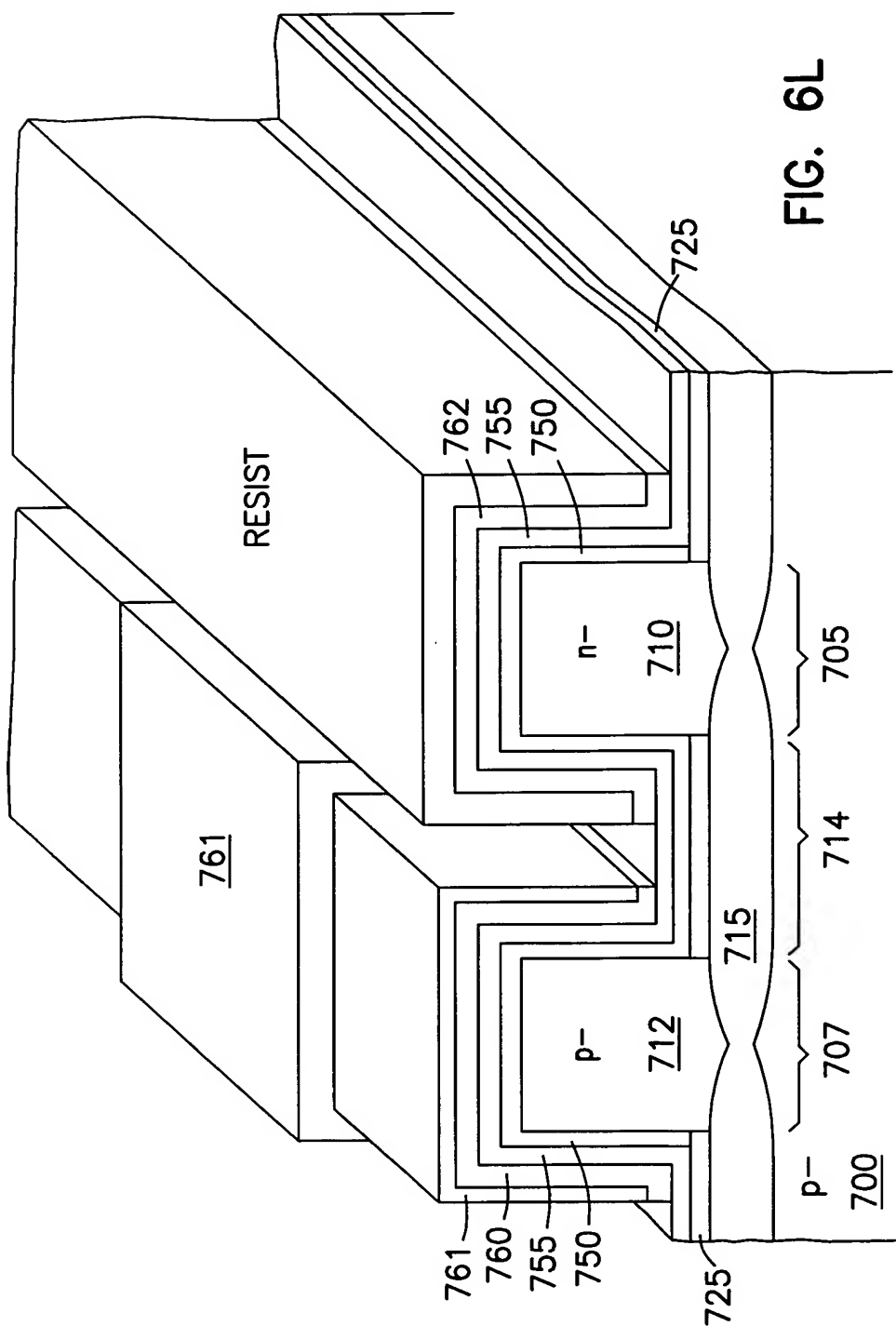
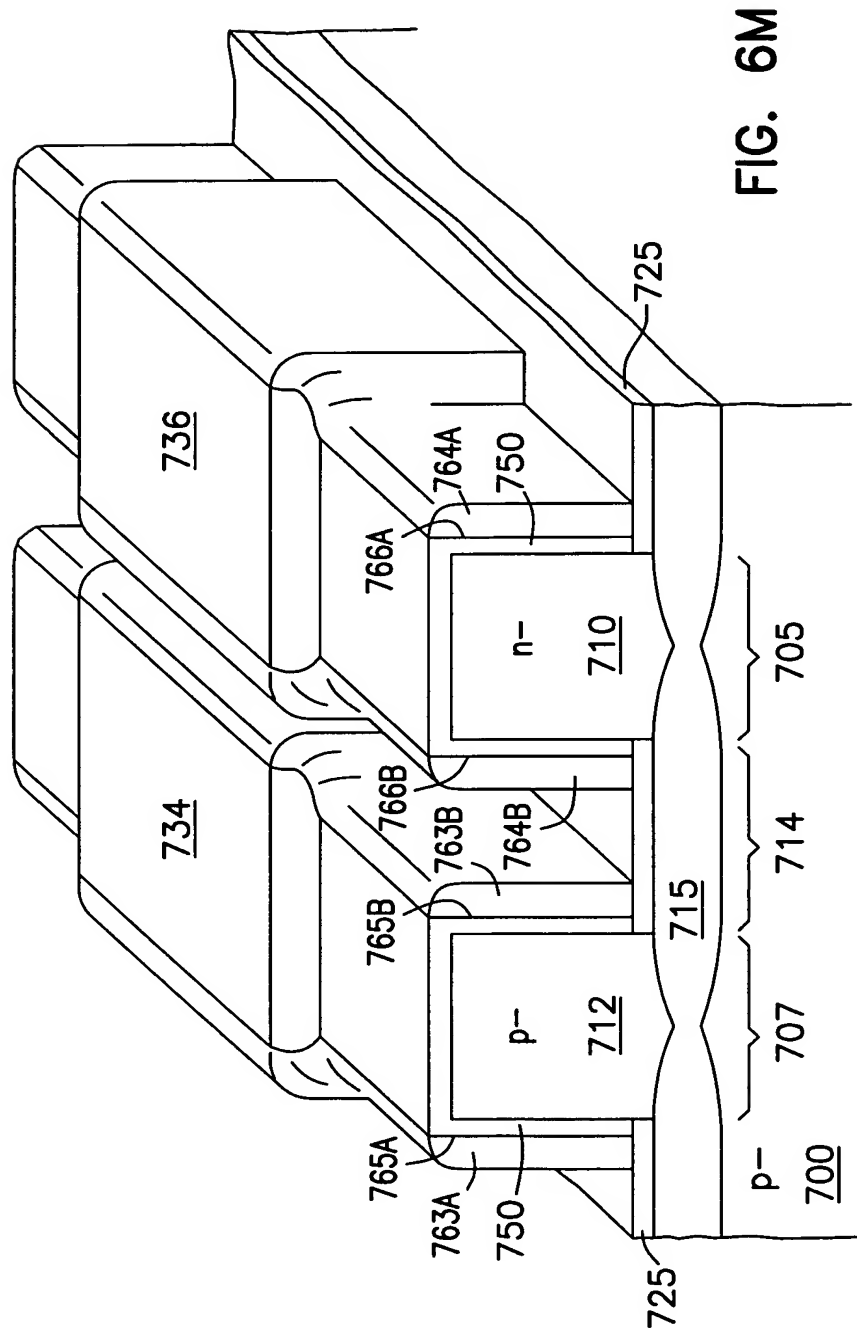
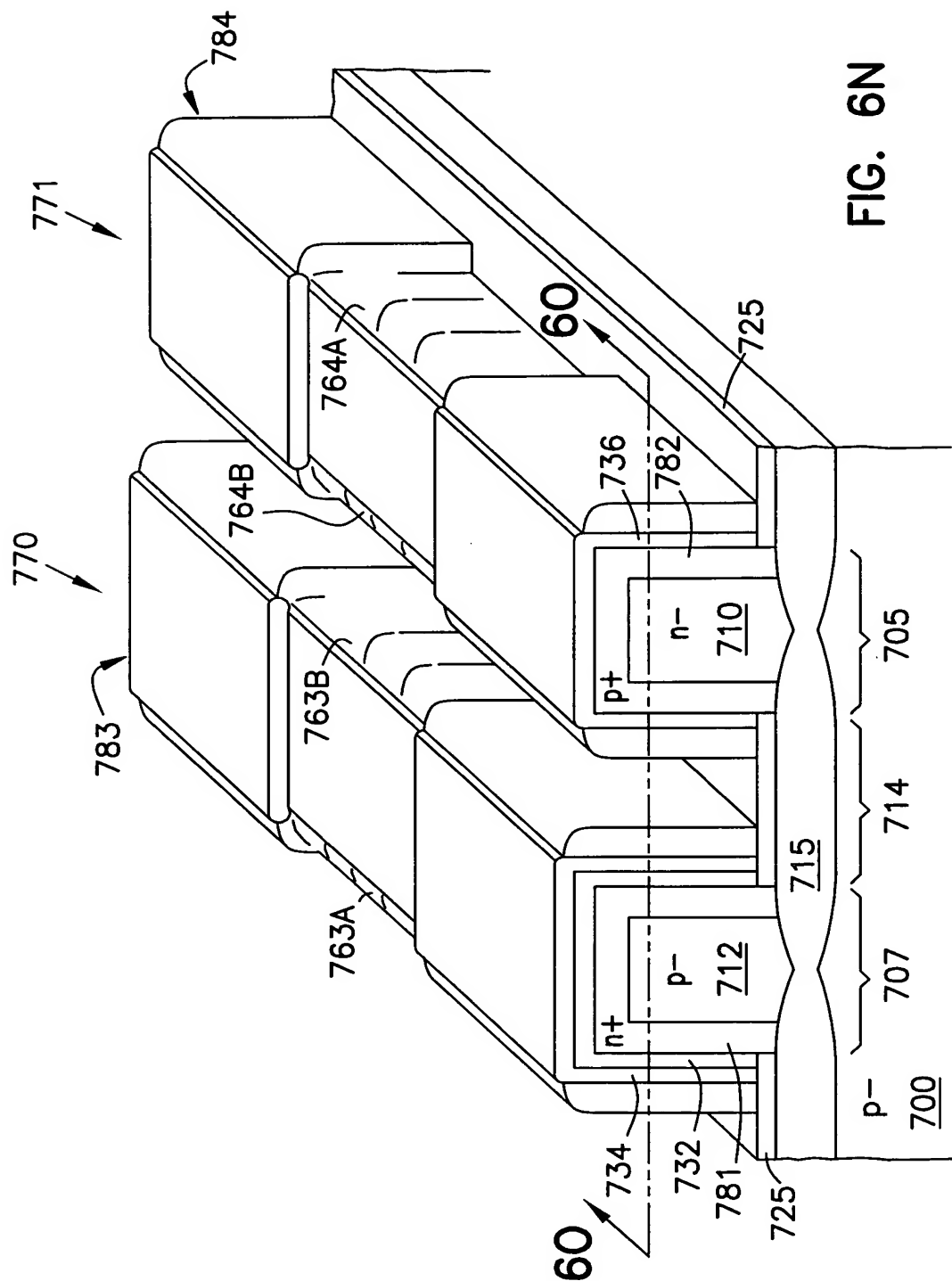


FIG. 6J









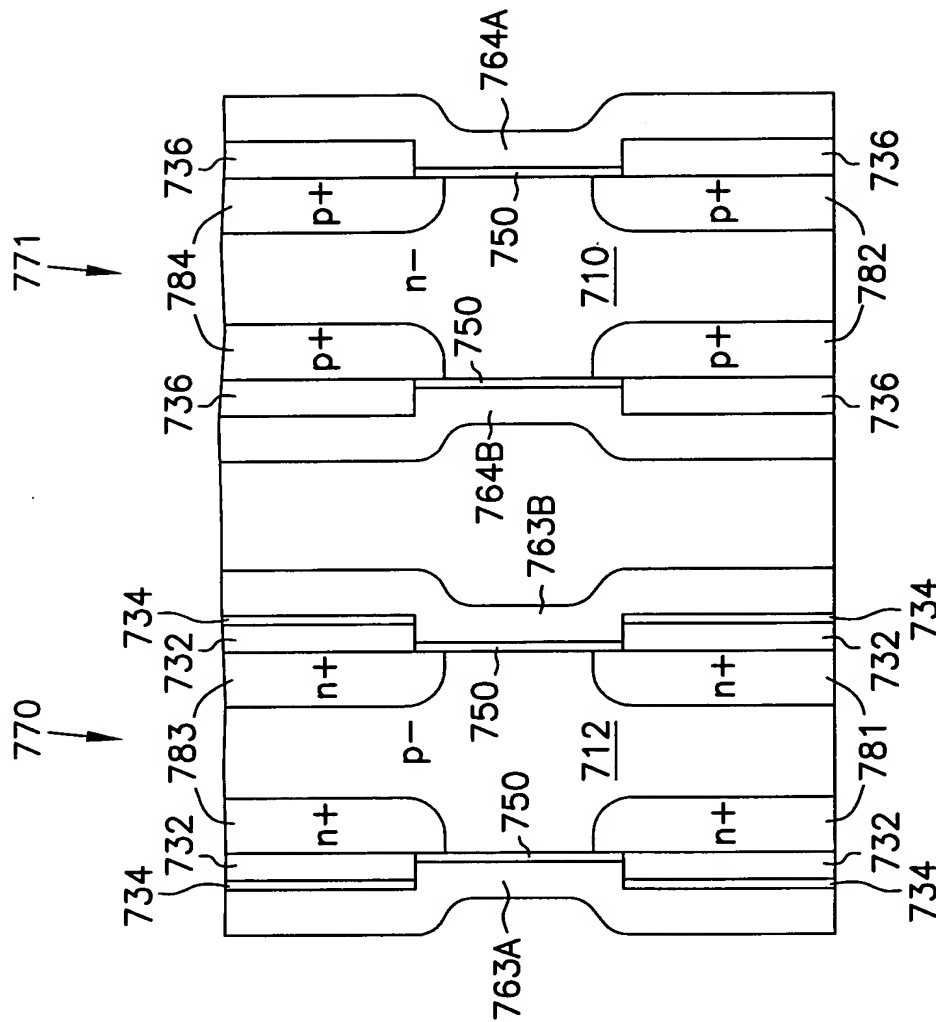


FIG. 60

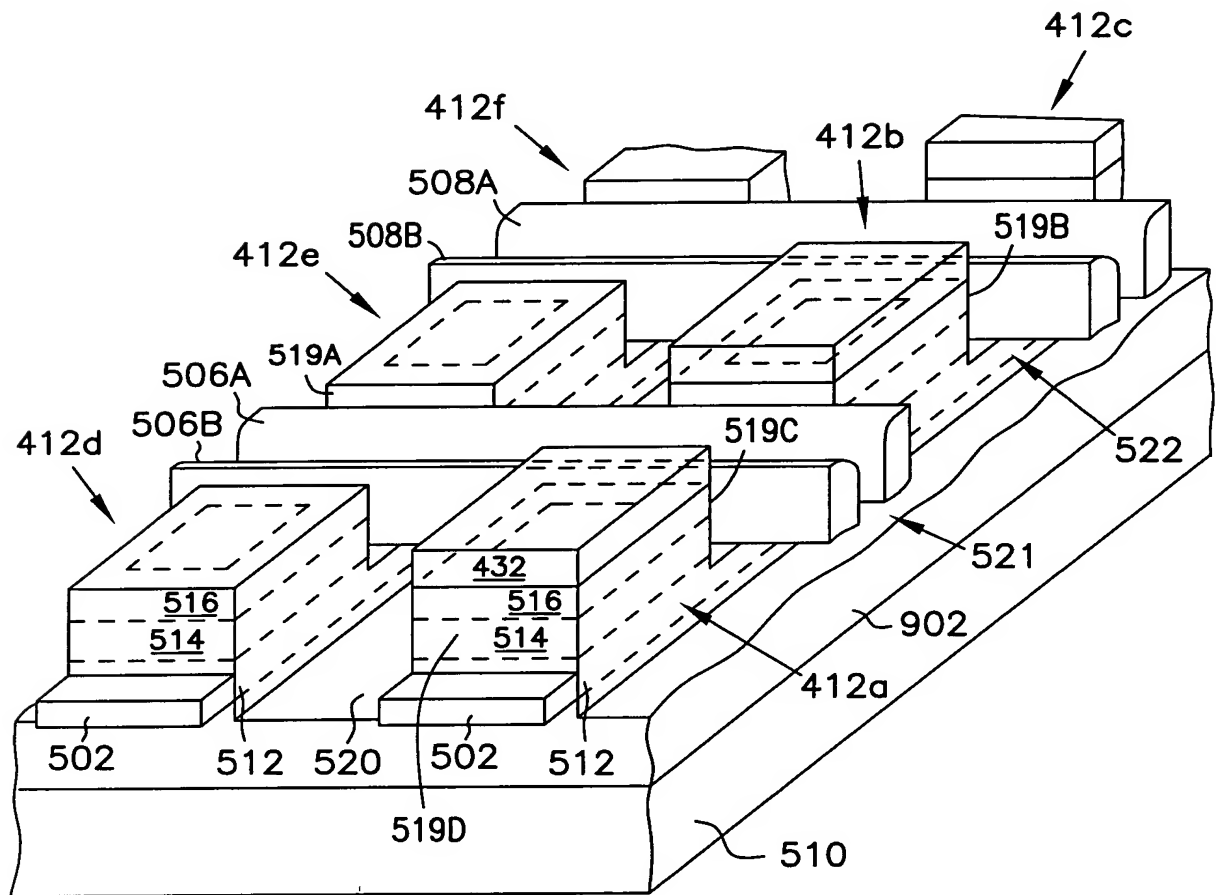


FIG. 7



